



PATENT  
Customer No. 22,852  
Attorney Docket No. 06720.0113

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:	)	
	)	
Ming-Dou KER et al.	)	Group Art Unit: 2835
	)	
Application No.: 10/727,550	)	Examiner: Unknown
	)	
Filed: December 5, 2003	)	
	)	
For: TURN-ON-EFFICIENT BIPOLAR	)	Confirmation No.: 8001
STRUCTURES WITH DEEP N-	)	
WELL FOR ON-CHIP ESD	)	
PROTECTION	)	

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)**

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(b), Applicants bring to the attention of the Examiner the documents listed on the attached PTO 1449. This Information Disclosure Statement is being filed before the mailing date of a first Office Action on the merits for the above-referenced application.

Copies of the listed documents, including any copending patent applications, are attached.

Applicants respectfully request that the Examiner consider the listed documents and indicate that they were considered by making appropriate notations on the attached form.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." If the Examiner applies any of the documents as prior art against any claim in the application and applicants determine that the cited documents do not constitute "prior art" under United States law, applicant reserves the right to present to the office the relevant facts and law regarding the appropriate status of such documents.

Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

Dated: July 7, 2004

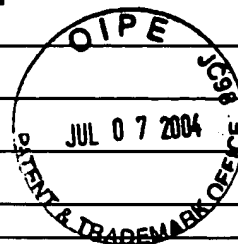
By: \_\_\_\_\_

  
Qingyu Yin\*

\*With limited recognition under 37 C.F.R. § 10.9(b)

## INFORMATION DISCLOSURE CITATION

Atty. Docket No.	06720.0113	Appln. No.	10/727,550
Applicant	Ming-Dou KER et al.		
Filing Date	December 5, 2003	Group:	2835



## U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate
	5,744,842	4/28/1998	Ker			
	6,072,219	6/06/2000	Ker et al.			

## FOREIGN PATENT DOCUMENTS

Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

C. Duvvury and A. Amerasekera, "ESD: A pervasive reliability concern for IC technologies," <i>Proc. of IEEE</i> , vol. 81, pp. 690-702, May 1993.
T.-Y Chen and M.-D. Ker, "Investigation of the gate driven effect and substrate-triggered effect on ESD robustness of CMOS devices," <i>IEEE Trans. on Device and Materials Reliability</i> , vol. 1, pp. 190-203, December 2001.

Examiner	Date Considered
*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	
Form PTO 1449	Patent and Trademark Office - U.S. Department of Commerce